

## TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

|                           |   |
|---------------------------|---|
| <b>Title of Invention</b> | SYSTEM AND METHOD FOR DIRECT WRITE TO DYNAMIC RANDOM ACCESS MEMORY (DRAM) USING PFET BIT-SWITCH |
| Application Number :      |   |
| Date :                    |   |
| First Named Applicant:    | Darren L. Anand   |
| Confirmation Number:      |   |
| Attorney Docket Number:   | BUR920020145US1   |

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| Submitted By:   | Elec. Sign.        | Sign. Capacity |
|---|--------------------|----------------|
| Mr. Robert A. Walsh Esq.<br>Registered Number: 26,516 | Robert A. Walsh /s | Attorney       |

| <b>Documents being submitted:</b>  | <b>Files</b>  |
|--|---|
| us-ids   | BUR920020145US1-usidst.xml<br>us-ids.dtd<br>us-ids.xsl  |
| us-assignment  | BUR920020145US1-usassn.xml<br>us-assignment.xsl<br>us-assignment.dtd<br>bur920020145assign1.tif<br>BUR920020145assign2.tif  |
| us-request   | BUR920020145US1-usrequ.xml<br>us-request.dtd<br>us-request.xsl  |
| us-fee-sheet   | BUR920020145US1-usfees.xml<br>us-fee-sheet.xsl<br>us-fee-sheet.dtd  |
| application-body   | BUR920020145US1SPEC-trans.xml<br>us-application-body.xsl<br>application-body.dtd<br>wipo.ent<br>mathml2.dtd<br>mathml2-qname-1.mod<br>isoamsa.ent<br>isoamsb.ent<br>isoamsc.ent<br>isoamsn.ent<br>isoamso.ent<br>isoamsr.ent<br>isogrk3.ent<br>isomfrk.ent<br>isomopf.ent<br>isomscr.ent<br>isotech.ent<br>isobox.ent<br>isocyr1.ent<br>isocyr2.ent<br>isodia.ent<br>isolat1.ent<br>isolat2.ent<br>isonum.ent<br>isopub.ent<br>mmlextra.ent<br>mmlalias.ent<br>soextblk.dtd<br>BUR920020145drawing1.tif<br>BUR920020145drawing2.tif |
| us-declaration   | BUR920020145dec1.tif  |
| us-declaration   | BUR920020145dec2.tif  |
| us-declaration   | BUR920020145dec3.tif  |
| <b>Comments</b>  |   |
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